

## ABSTRACT OF THE DISCLOSURE

In a semiconductor integrated circuit having a functional macro,  
5 plural first and second power lines extending over the functional macro and  
supplying first-level and second-level voltages respectively to the  
functional macro are electrically connected through plural first and second  
power terminal patterns to plural third and fourth power lines extending  
over the semiconductor integrated circuit in the second direction and  
10 supplying the first-level and second-level voltages respectively to the  
semiconductor integrated circuit.